

# Design and Simulation of a Chopper Series for a Photo-Voltaic Generator Functioning at its Maximum Power

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**Abstract**— *Static converters are used in various fields of the conversion of electrical energy. The significant development of electric power switches and variety of design techniques of control and regulation circuits provide very advantageous solutions for the congestion, reliability, performance and maintenance of converters. This work focuses on the study and simulation of a chopper Series, and its association with a photovoltaic generator for the purpose of a Maximum Power in Operation issued by the latter. The principle of the chopper control is based on the variation of the duty ratio, based on a percentage of the open circuit voltage of a solar cell driver. The operation control system helps the photovoltaic system to function at its maximum power.*

**Keywords**— Static converter, Chopper, Thyristor, Control circuit, Photovoltaic generator, Maximum power point.

## I. Introduction

The optimization of photovoltaic generators performance and congestion perspective requires rigorous design methods, and the use of control systems and distribution of electrical energy more appropriately (Salameh, 1991; Schaefer, 1984 ;Issaadi, 2006). The technique of Maximum Power Point supplied by a photovoltaic generator is based on the introduction of a chopper between the generator and the storage battery or between the generator and the load directly (Merat, 1984; Séguier 1987; Champenois 1988; Dalmaso, 1988; Lander, 1989). The use of a thyristor-based on a converter allows a high power application, hence the interest of a tracking system of the maximum power point, which revolves around a dedicated control room (Schaefer, 1984; Salameh, 1991; Salas, Olias, Barrado and Lazaro, 2006; Gori, 2011; Azdani, 2009; Merah, 2010; Benkhelil et Gherbi, 2011; Djafour,

2014). The principle of control is to vary the value of the duty ratio based on a fraction of the open circuit voltage of a solar cell pilot (image of the voltage at the maximum power point). A switching power supply is essentially a DC-DC converter to control the amplitude of the output voltage (Fogelman, 1982; Champagne, 1988; Won and Kim, 1994; Hsiao and Chen, 2005; Tahri et Benyoucef, 2010; Ait-Cheikh, 2007). As in any power electronics, semiconductor components used in electronic switch to maximize yield. The use of switching power transistors limits the chopping frequency, but the use of power MOS transistors and thyristors raises this limit, which greatly reduces component size (Raymon, 1983; Makhoulouf, 2006). The use of auxiliary thyristors in the logic using circuit switching defused the main thyristor as quickly as we wish and therefore provides for low output voltages of the low currents (very interesting case for engine starting system series excitation) (Ramirez, 1980). In the low power range at medium power, we often need a continuous supply which has negligible ripple alternative but adjustable amplitude, however. For such applications, are used to power divisions (Agati, 1997). Closing and rapid opening of the switch gives a chopped voltage between the source and level of zero. This voltage is applied to an LC filter which smooth and applies a level substantially constant voltage to the load. Cutting (opening / closing) fast input voltage controls the output voltage that the average voltage output is a function of the duty cycle (Koutroulis, Kalaitzakis and Voulgaris, 2001; Hohm and Ropp, 2003; Calderon, 2006; Park, Ahn, Cho and Yu, 2006; Femia, Petrone, Spagnuolo and Vitelli, 2010; Poshtkouhi, Palaniappan, Fard and Trescases, 2011; Pilawa-Podgurski Robert, Perreault, 2013). The power circuit of the chopper being known, the aim of this study is to propose a

control circuit for controlling the switches by implementing this system in software to visualize the operation curves. This article on the study and the simulation of a series chopper for operation at maximum power from the PV array and its application in the case of the direct association of a DC motor of a submersible pump Type series in its joints, first presents the material and methods title that shows in detail the control circuit of a switching power supply, then the title results and discussion and finally concluded.

## II. Materials and Methods

### II.1 Materials

As part of this work, we used the Proteus Version 8.1 software was used for the simulation.

### II.2 Methods

#### II.2.1 The switching chopper principle

Priming the main thyristor  $Th_1$  should not take place before the load capacitor  $C_d$  and reversing the polarity at its terminals. Priming of the auxiliary thyristor  $Th_2$  creates a brief current which ensures the charge of the capacitor  $C_d$  in the supply voltage  $U_e$ . Thyristor  $Th_2$  turns off by itself when the current  $I_c$  is zero, booting the second auxiliary thyristor  $Th_3$ , ensures polarity reversal of the capacitor  $C_d$  through the inductance  $L_d$ . The main thyristor  $Th_1$  can be engaged from time  $t_2$ . To lock, simply start the auxiliary thyristor  $Th_2$ . The frequency of the ripple applied to the  $L_2$ - $C_2$  output filter is very high; as  $L_2$  and  $C_2$  values can be low, while allowing to greatly reduce the ripple. The diode provides a path to direct current in the charging coil when the switch is open. Can control the magnitude of the charging voltage to any desired level.

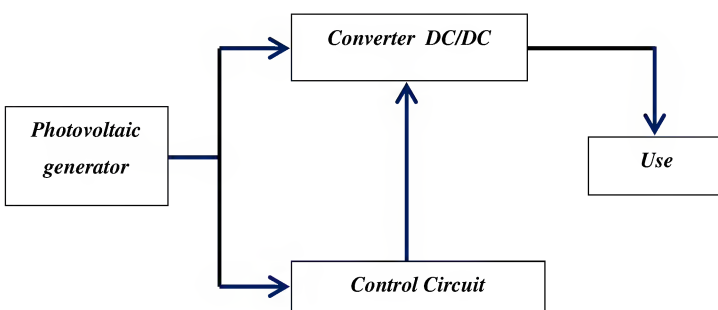


Figure 1: Block diagram General of a series chopper.

#### II.2.2 Control circuit chopper

The principle of the control circuit is to generate the necessary signals to the Thyristor firing, while observing the conditions of synchronization pulses during normal operation. The control signals resulting from the comparison of a saw tooth wave at a predefined reference voltage (case of  $Th_2$  and  $Th_3$ ) and a variable reference voltage (case  $Th_1$ ).

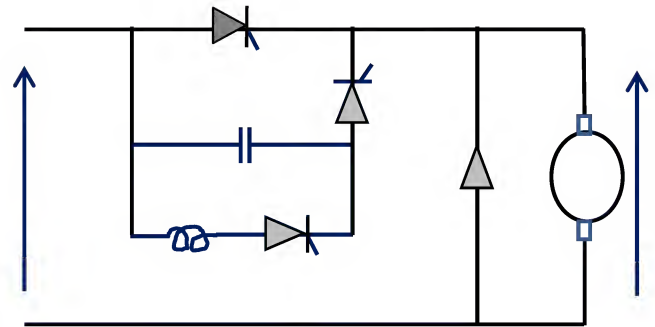


Figure 2: Chopper series.

The equation governing the oscillating circuit ( $L_d$ ,  $C_d$ ) is:

$$U_{Cd} + L_d C_d \frac{d^2 U_{Cd}}{dt^2} = 0$$

The solution of this equation is of the form:

$$U_{Cd}(t) = U_m \cos \omega t \text{ where } \omega^2 L_d C_d = 1$$

The capacitor requires a current:

$$I_{Cd}(t) = C_d \frac{d(U_{Cd})}{dt}$$

This transitional period lasts a half period  $T_d/2 = 2\omega/2 = \omega$

Hence at this point we polarity reversal:

$$U_{Cd}(t_2) = U_e \cos \omega t_2 = U_e \cos \pi = -U_e$$

The main thyristor  $Th_1$  can be engaged from time  $t_2$ . To lock, simply start the auxiliary thyristor  $Th_2$ .

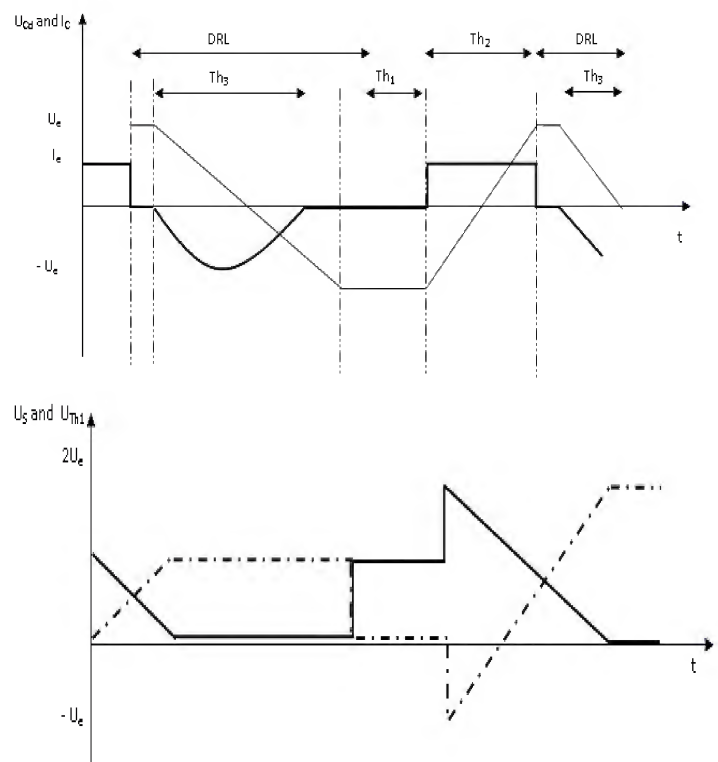


Figure 3: Timing diagrams of voltages and currents of the series chopper.

### II.2.3 Dimensionnement extinguishing system

The value of  $C_d$  capacity must be chosen so that the protection time ( $t_f$ ) is guaranteed to put out the maximum current  $I_s$  debited through the chopper. ' $t_f$ ' is determined by the equation defining the voltage across the main thyristor during the switching phase.

$$U_C = U_{Th1} = I_s (t - t_4) / C_d - U_e$$

$U_{Th1}$  remains negative until time  $t_5$  (as  $t_4 - t_5 = t_C$ )

$$t_f = C_d U_e / I_s$$

### II.2.4 Control circuit chopper

The principle of the control circuit is to generate the necessary signals to the thyristor firing, meanwhile observing the conditions of synchronization pulses during normal operation. Cancels the signals resulting from the comparison of a sawtooth wave voltage to a predefined reference (case of  $Th_2$  and  $Th_3$ ) and a variable reference voltage (case of  $Th_1$ ).

## III. Experimental results

The following figures show the different timing of the control circuit implemented in the Proteus software to visualize the evolution of different voltages.

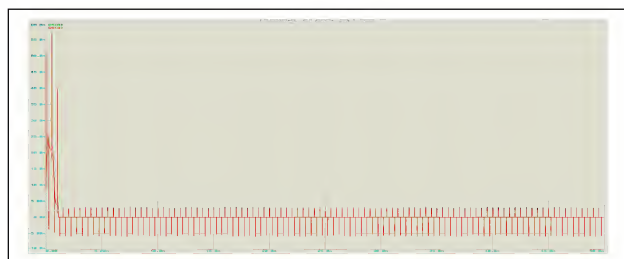


Figure 4: Terminal voltage of the main thyristor  $Th_1$ .

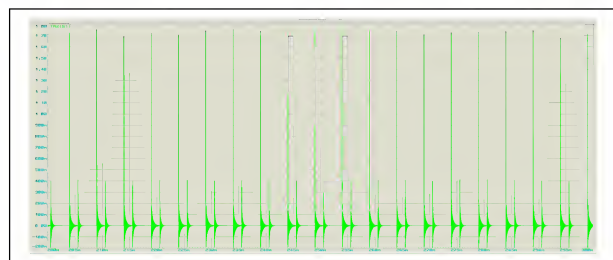


Figure 5: Terminal voltage of the auxiliary thyristor  $Th_2$ .

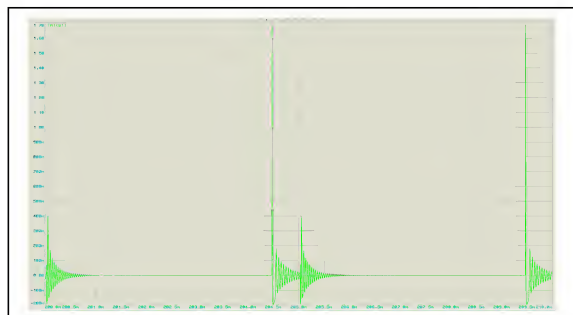


Figure 6: Terminal voltage of the auxiliary thyristor  $Th_3$ .

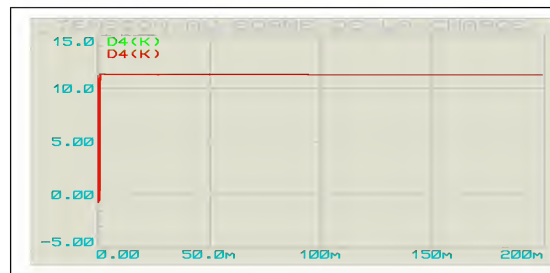


Figure 7: Voltage across the load for a duty ratio  $\alpha = 0.7$ .

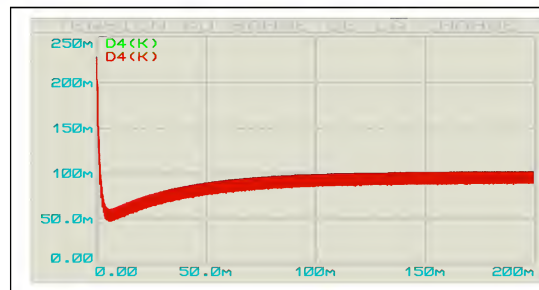


Figure 8: Voltage across the load for a duty ratio  $\alpha = 0.3$ .

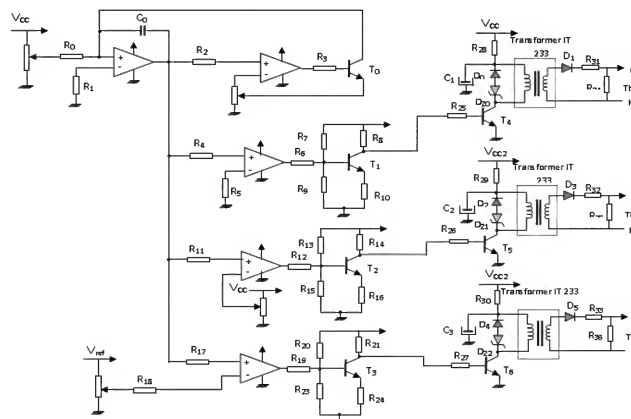


Figure 9: Chopper control circuit series for a PV generator.

## IV. Conclusion

The study and the simulation of a series chopper for operation at maximum power of a PV generator satisfactory results. Of cause, it would be boring testing the system on real load to assess the direct association of the solar generator with a DC motor. It will identify opportunities to use manual control for engine start (starting current limitation) and then switch to a standalone mode, once the duty ratio reaches a certain value and see how to make regulation the duty cycle for proper operation of the pump.

## References

- i. Ait-Cheikh S. M., «Etude, Investigation et conception d'algorithmes de commande appliqués aux systèmes photovoltaïques», Thèse de Doctorat d'état, Ecole Nationale Polytechnique, Alger, Algérie, 2007.



- ii. Azdani A., «A Control Methodology and Characterization of Dynamics for a Photovoltaic (PV) System Interfaced With a Distribution Network», *IEEE Trans On Power Delivery*, 2009; 24(3):1538-1555.
- iii. Benkhelil E. et Gherbi A., «Modélisation et simulation d'un générateur photovoltaïque avec
- iv. Calderon A. J., Vinagre B. M., Feliu V., "Fractional order control strategies for power electronic buck converters", *Signal Processing*, Vol.86, pp.2803-2819, 2006
- v. Champenois A., *Alimentations Thyristors et Optoélectronique*, 1ère édition, Edition du renouveau pédagogique, INC, Canada, pp. 289-402, 1988.
- vi. Coeurdacier S., *Electronique (les composants discrets non linéaires)*, Dunod, Paris, pp. 205-257, 1979.
- vii. Dalmasso J. L., *Cours d'électrotechnique2. Traitement de l'énergie électrique (convertisseurs statiques)*, Technique supérieur, France, PP: 341-357, 1988.
- viii. Djafour Ahmed, «Modélisation de la caractéristique de sortie d'un module photovoltaïque installé à Ouargla» *Annales des Sciences et Technologie*, Vol. 6, n° 2, 2014.
- ix. édition, DUNOD, Paris- France, pp. 139-146, 1997.
- x. Femia N, Lisi G, Petrone G, Spagnuolo G, Vitelli M., « Distributed maximum power point tracking of photovoltaic arrays: novel approach and system analysis», *IEEE Trans Ind Electr* 2008; 55(7).
- xi. Femia N, Petrone G, Spagnuolo G, Vitelli M., «A new analog MPPT technique», *Progress in photovoltaics: research and applications*, vol. 18. Wiley-Blackwell; 2010. p. 28-41.
- xii. Fogelman T., *Systèmes photovoltaïques pour les pays en développement*, Marseille, France, 231 P, 1982.
- xiii. Gori B., « Modélisation et simulation d'un système PV adapté par une commande MPPT analogique », *Mémoire d'Ingéniorat*, Université Ouargla, 2011.
- xiv. Hohm and Ropp, «Comparative study of maximum power point tracking algorithms», *Prog Photovoltaics Res Appl* 2003; 11(1): 47-62.
- xv. Issaadi S., « Commande d'une poursuite du point de puissance maximum (MPPT) par les Réseaux de Neurones », *Mémoire de magister*, Ecole Nationale Polytechnique, Alger, Algérie, 2006.
- xvi. Jiang J-A, Huang T-L, Hsiao Y-T. and Chen C-H., «Maximum power tracking for photovoltaic power systems», *In J. of Science and Engineering*. 2005; 8(2):147-153.
- xvii. Koutroulis E, Kalaitzakis K, Voulgaris NC., «Development of a microcontroller-based, photovoltaic maximum power point tracking control system», *IEEE Trans Power Electr* 2001;16(1):46-54.
- xviii. Lander C. W., *Electronique de Puissance*, 2ème édition, Mc Graw-Hill, Paris, P. 147-150, P. 191-197, P. 174-175, 1989.
- xix. Makhlouf M., «Etude et optimisation d'un modèle de conversion d'énergie photovoltaïque application au pompage», *mémoire de magister*, université Mentouri Constantine, Algérie, 2006.
- xx. Marston R. M., *Etudes pratiques de l'amplificateur opérationnel*. 3ème édition, Eyrolles, Paris-France, PP:1-54, 1981.
- xxi. Merahi R., «Modélisation et Simulation d'un Module PV par Matlab», *Journal of Scientific Research* vol. 1, 2010.
- xxii. Merat R., Moreau R., Allay L., Dubois J.P., La Fargue J. et Le Goff R., *Electronique de puissance*, Nathan, Paris, PP : 1-78, 1984.
- xxiii. Park J-H, Ahn J-Y, Cho B-H, Yu G-J., « Dual-module-based maximum power point tracking control of photovoltaic systems», *IEEE Trans Ind Electr* 2006; 53(4): 1036-47.
- xxiv. Patcharaprakiti N., Premrudeepreechacharn S., Sriuthaisiriwong Y., «Maximum power point tracking using adaptive fuzzy logic control for grid connected photovoltaic system», *Renewable Energy*, 2005; 30(11): 1771-1788.
- xxv. Perez-Mas et Fouchet, *Electronique Pratique*, 1ère édition, BORDAS, Paris, pp. 190-199, 1984.
- xxvi. Pierre Agati, *Electricité, Electronique (de commande de puissance) et Electrotechnique*, 1ère édition, DUNOD, Paris-France, pp. 43-78, 1997.
- xxvii. Pilawa-Podgurski Robert CN, Perreault David J., «Submodule integrated distributed maximum power point tracking for solar photovoltaic applications», *IEEE Trans Power Electr* 2013; 28(6): 2957-67 .
- xxviii. Poshkouhi S, Palaniappan V, Fard M, Trescases O., «A general approach for quantifying the benefit of distributed power electronics for fine grained MPPT in photovoltaic applications using 3D modeling», *IEEE Trans Power Electr* 2011; 99(11): 4656-66.
- xxix. Raymon M. et Marton, 110. *Etudes pratiques à semi-conducteurs*, Eyrolles, Paris, France, pp. 4 - 69, 1983.
- xxx. Salameh Z.M., 'Step Down Maximum Power Point Tracker for Photovoltaic System', *Solar Energy*, Vol. 46, N°5, pp. 279-282, 1991.
- xxxi. Salas V., Olias E., Barrado A. and Lazaro A., «Review of the Maximum Power Point Tracking Algorithms for Stand-Alone Photovoltaic Systems», *Solar Energy Materials & Solar Cells*, 2006; 90(11):1555-1578.
- xxxii. Santos L. J.L., Antunes. F, Chehab A. and Cruz C., «A Maximum Power Point Tracker for PV Systems Using a High Performance Boost Converter», *Solar Energy*, 2006; 80(7): 772-778.
- xxxiii. Schaefer J.F., 'An Inexpensive Photovoltaic Pseudo-maximum Power Point Tracker', *New Mexico Solar Energy Institute*, IEEE, pp. 643-646, 1984.
- xxxiv. Séguier G., *Les Convertisseurs de l'Electronique de Puissance - La Conversion Continu - Continu*, Ed. Tech. & Doc., Tome 3, 1987.
- xxxv. Tahri K. et Benyoucef B., « Etude et Modélisation d'un Générateur Photovoltaïque », *Journal Scientifique de Recherche sur la Physique Energétique*, Vol.1, 2010.
- xxxvi. un étage d'adaptation DC/DC», *Revue des Energies Renouvelables ICESD'11 Adrar* 159 - 170, 2011.
- xxxvii. Won C.Y., Kim D.H., Kim S.C., Kim W.S. and Kim H.-S., « A new maximum power point tracker of photovoltaic arrays using fuzzy controller», *In 25th Annual IEEE Power Electron.* 1994, p. 396-403.
- xxxviii. ANNEX. A chopper control circuit series for a photovoltaic generator functioning at its maximum power.